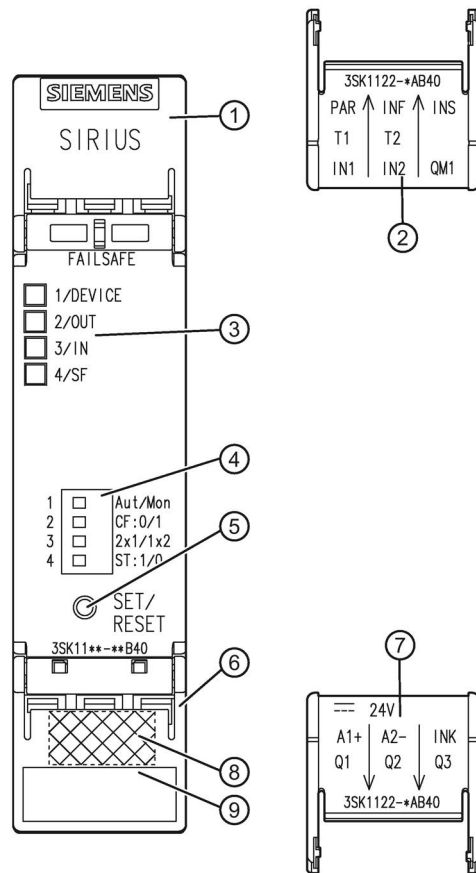


5.2.8.2 Design



- ① Top cover flap
- ② Top cover flap; internal inscription
- ③ Display LEDs
- ④ DIP switch
- ⑤ SET/RESET button
- ⑥ Bottom cover flap
- ⑦ Bottom cover flap; internal inscription
- ⑧ DataMatrix code
- ⑨ Device identification label

5.2.8.3 Terminal assignment

Terminal	Explanation
A1	+
A2	-
IN1	Sensor channel 1
IN2	Sensor channel 2
PAR	NO contact/NC contact evaluation
INS	START pushbutton circuit
INF	Feedback circuit
INK	Cascading circuit
T1	Test output 1 (for IN1, PAR)
T2	Test output 2 (for IN2, INF)
Q1	Safety-related output 1 (switching to P potential, 24 V DC)
Q2	Safety-related output 2 (switching to P potential, 24 V DC)
Q3	Safety-related output 3 (switching to P potential, 24 V DC)
QM1	Semiconductor signaling circuit 1 (switching to P potential, 24 V DC)

State diagrams 3SK1122-xAB40

